

In the Specification:

Please replace the paragraph beginning on page 6, line 25 with the following amended paragraph:

In Fig. 1, a first power supply, such as a luminance inclination circuit 1- circuit 1, changes a voltage of a gate-on power supply  $V_{on}$  for activating gate lines, in synchronization with an output of a gate driving circuit 4, and outputs a gate-on power supply for gate driving circuit as shown by the power supply  $V_{gon}$  in Fig. 5. The use of this gate-on power supply for gate driving circuit makes it possible to intentionally blunt falling edges of outputs (gate driving voltages)  $V_{OUTi}$  ( $i$  is a suffix and a natural number:  $i = 1$  to  $n$ ) of the gate driving circuit 4. This can reduce luminance unevenness in a direction in which the gate lines extend.

Please replace the paragraph beginning on page 7, line 11 with the following amended paragraph:

A second power supply, such as a power holding circuit 2- circuit 2, holds a power fed by the gate-on power supply  $V_{on}$  for a predetermined period of time and it is constituted using capacitors or the like having a sufficiently large capacity.